

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims:

1. (Currently Amended) An apparatus for loading computer code from a memory type integrated circuit card preloaded with computer code, said apparatus comprising:

a card interface capable of distinguishing between a conventional integrated circuit card and said memory card, said card interface having a low speed first data port for transferring data in accordance with a first standard and a high-speed second data port for transferring data in accordance with a second standard;

a memory for storing computer code for execution by the apparatus; and

a microcontroller coupled to the card interface and to the memory for, if said card is a memory card, reading said computer code from said memory card by way of said high-speed second data port to said memory, for thereby updating the computer code stored in said memory so as to effect a change of the functional operation of the apparatus.

Claims 2-4 (Canceled)

5. (Previously Presented) The apparatus of claim 1, wherein said card interface comprises:

means for producing a first signal that is coupled to an integrated circuit card connection; and

means for analyzing a second signal that is produced by a said memory card in response to said first signal.

6. (Original) The apparatus of claim 5, wherein said second signal is not produced by integrated circuit cards that are not memory cards.

7. (Previously Presented) The apparatus of claim 5, wherein said card interface applies said first signal to a clock signal connector of said integrated circuit card connection and receives said second signal on a data input/output signal connector of said integrated circuit card connection.

8. (Canceled)

9. (Currently Amended) The apparatus of claim 1 wherein said card interface further comprises:

means for transferring computer code from said memory card to said computer controlled device memory unit.

10. (Currently Amended) The apparatus of claim 1, wherein said card interface microcontroller further comprises:

means for accepting or rejecting the computer code for transference from said memory card to said computer controlled device memory unit.

11. (Currently Amended) A method of loading computer code in a computer controlled device having a smart card interface for receiving a smart card, said card interface having a first data port for transferring data in accordance with a first standard and a second data port for transferring data in accordance with a second standard, said method comprising the steps of:

identifying whether the smart card is a memory card containing a memory unit with preloaded computer code, and a memory controller, or a conventional integrated circuit card; and

if said card is a memory card, transferring the computer code in said memory card through [a high speed] said second data port of a memory card into said computer controlled device; such that the transferred computer code is stored in a memory so as to effect a change in the functionality of the computer controlled device.

12. (Previously Presented) The method of claim 11 wherein said identifying step further comprises the steps of:

applying a first signal to said memory card;

analyzing a second signal produced by said memory card in response to said first signal to determine if said ~~integrated circuit~~ smart card is a memory card.

13. (Previously Presented) The method of claim 12 wherein said transferring step further comprises:

activating an NRSS interface.

14. (Original) The method of claim 11 further comprises:
analyzing a header of said computer code to determine the validity of the computer code.

15. (Original) The method of claim 11 further comprising toggling a reset signal.

16. (Previously Presented) The method of claim 15, further comprising said memory card monitoring a clock imputer terminal for a first signal in response to said toggled reset signal.

17. (Previously Presented) The method of claim 16, wherein said memory card generates a second signal in response to detection of said first signal.

18. (Cancelled)